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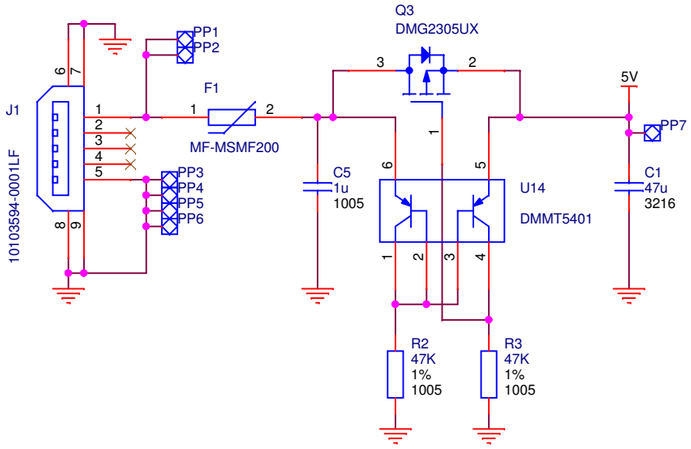

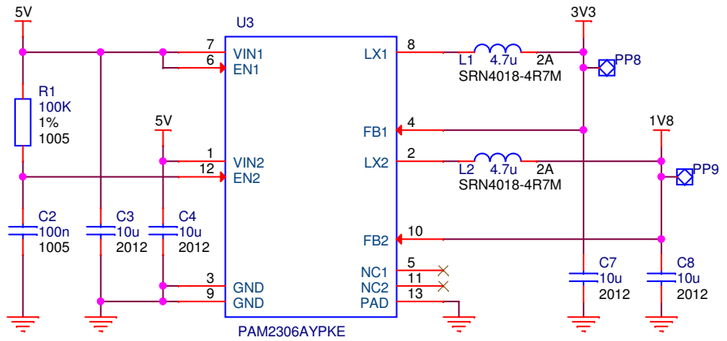

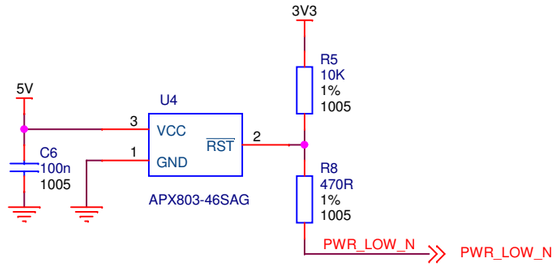

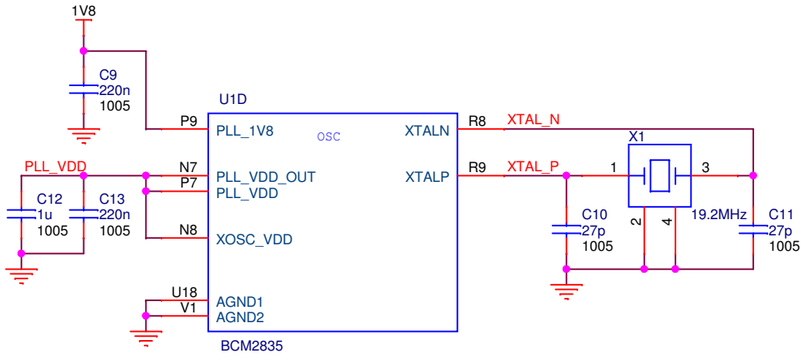

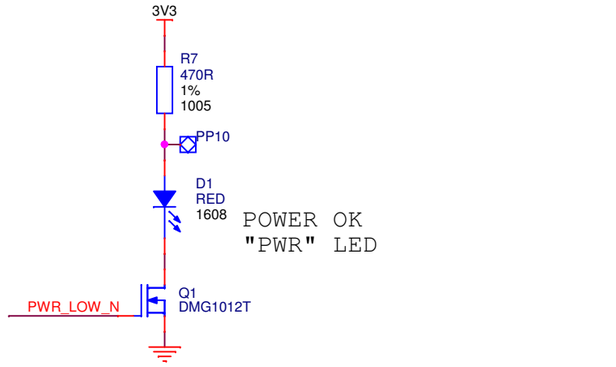

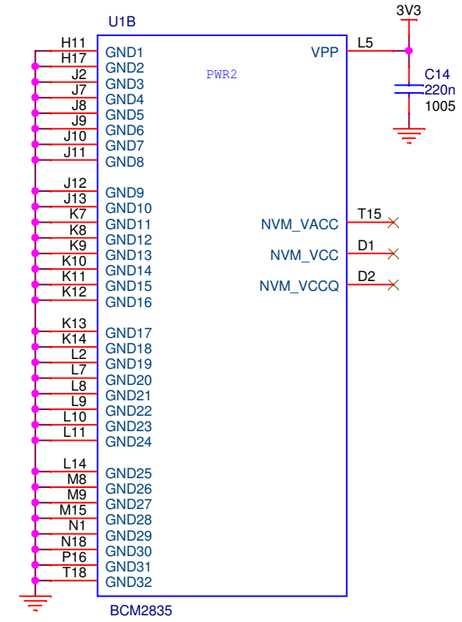

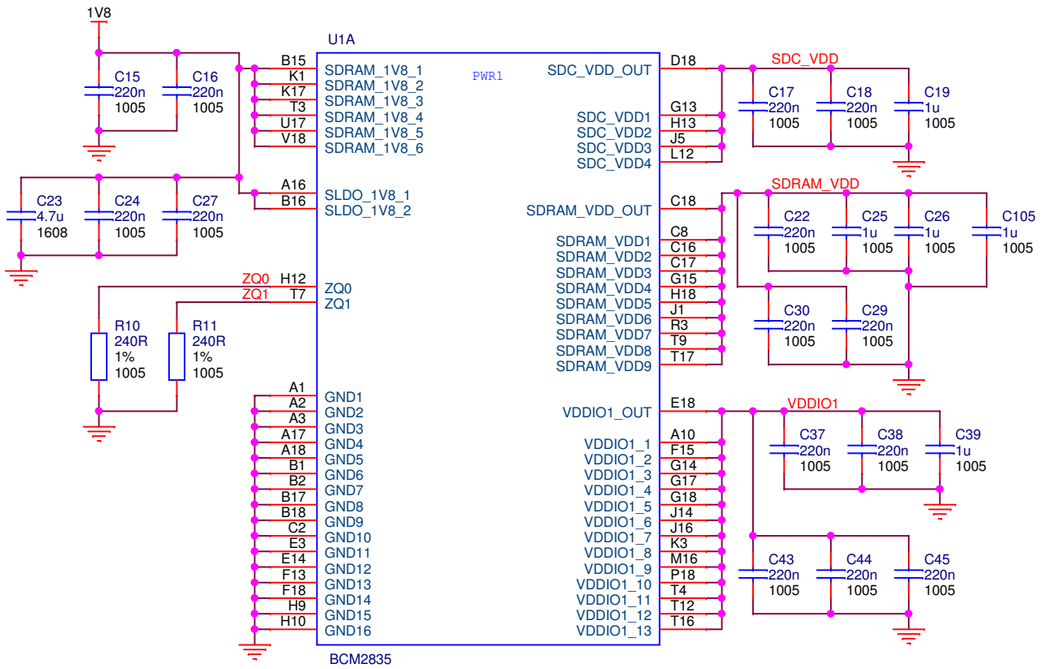

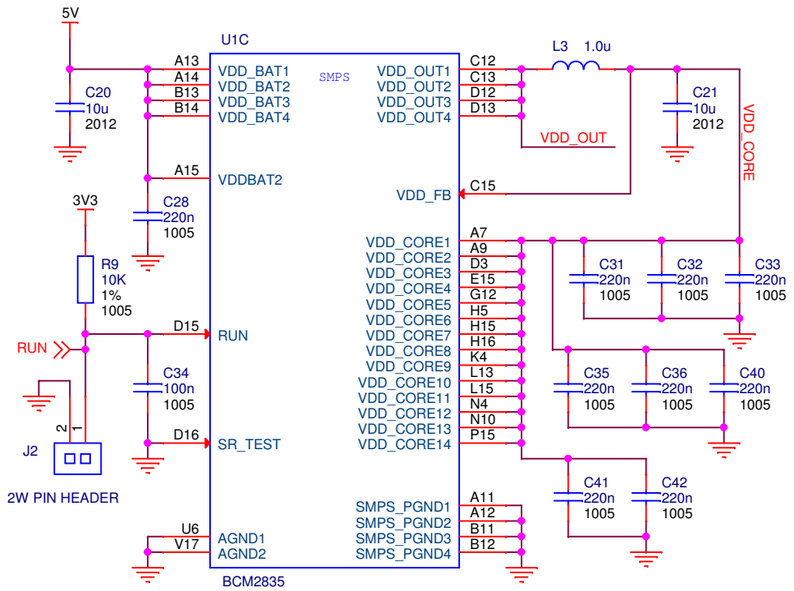

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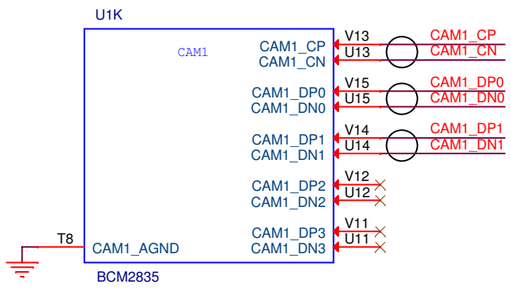

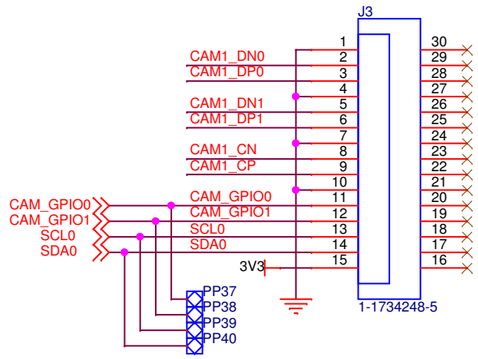

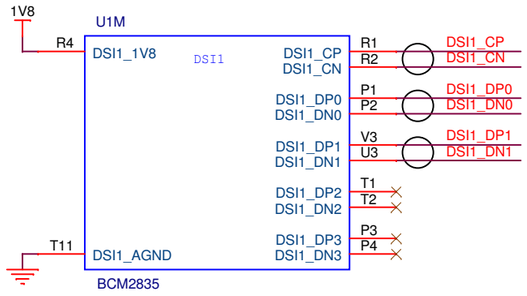

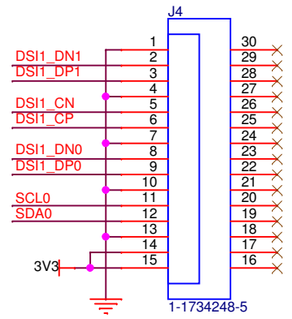

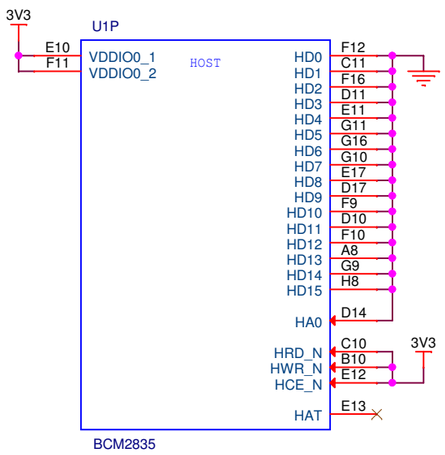

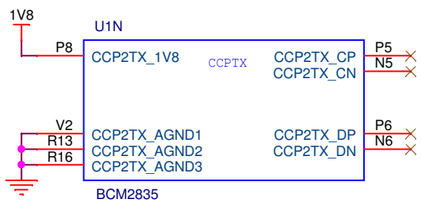

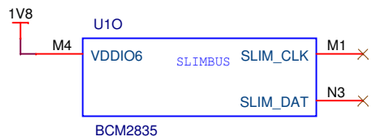

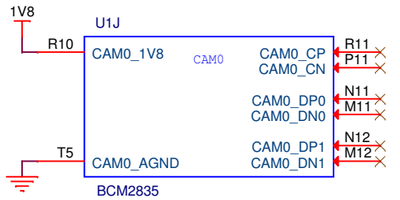

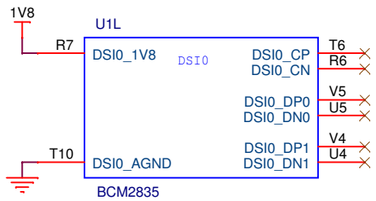

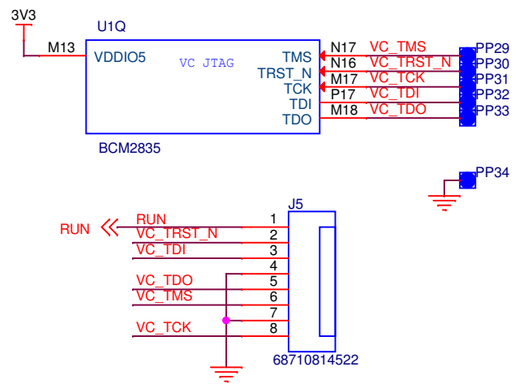

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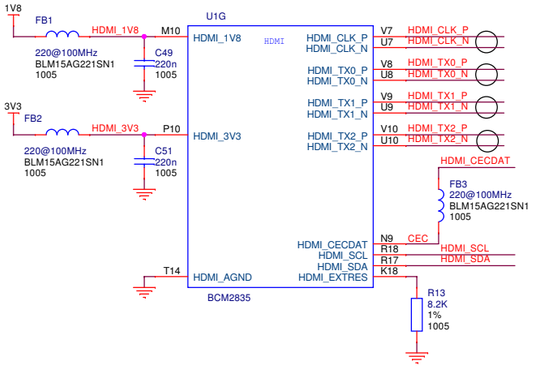
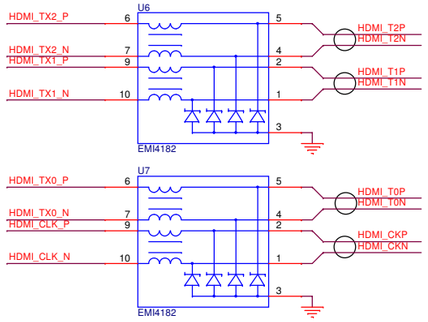
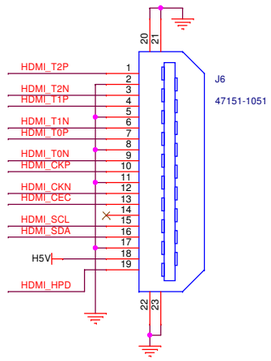
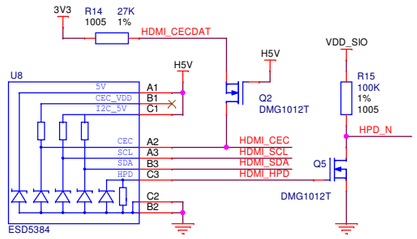
Route ringed signals as matched length 100R differential pairs

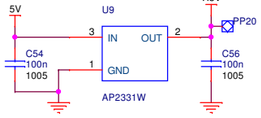
STATUS "ACT" LED

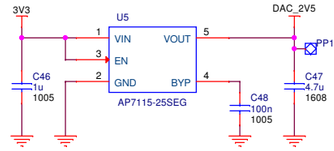


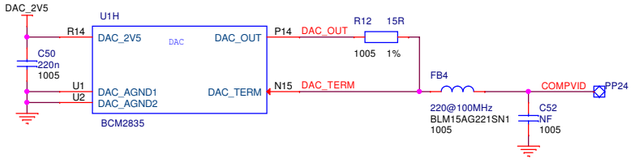


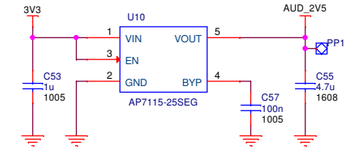


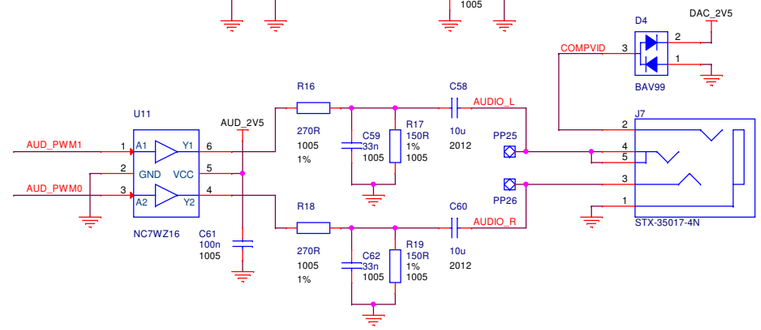


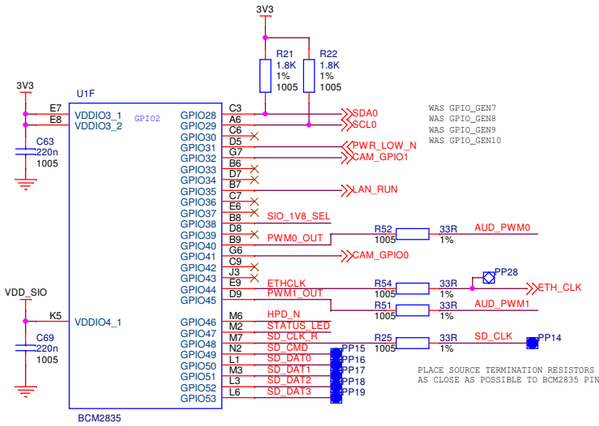


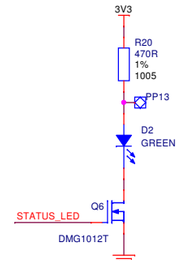


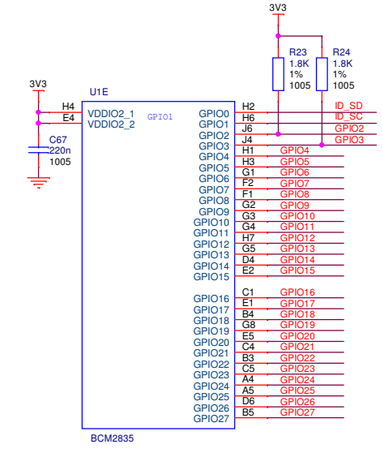


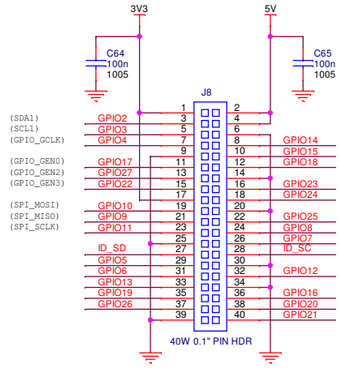










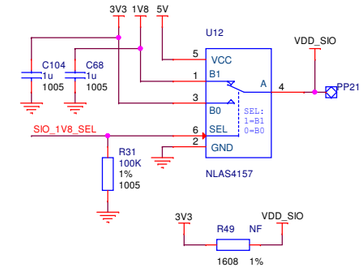


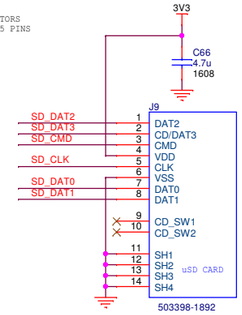
ID_SD and ID_SC PINS:

These pins are reserved for PiPlate ID EEPROM.

At boot time this I2C interface will be interrogated to look for an EEPROM that identifes the board and allows automagic setup of the GPIOs (and drivers).

DO NOT USE these pins for anything other than attaching an I2C ID EEPROM. Leave unconnected if ID EEPROM not required.





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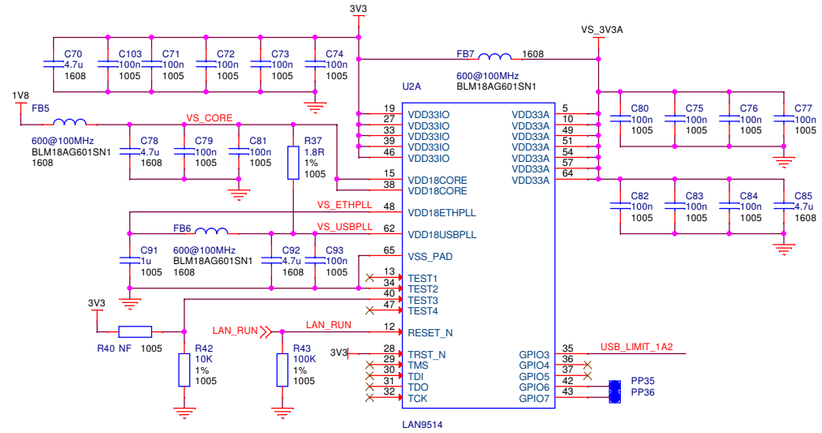

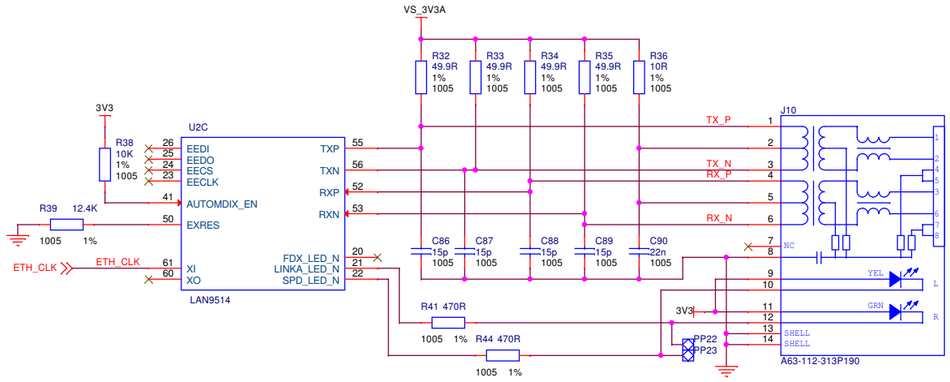

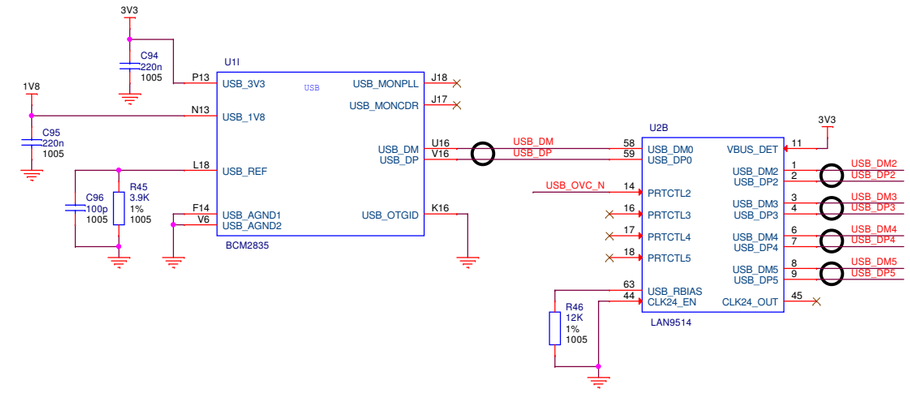

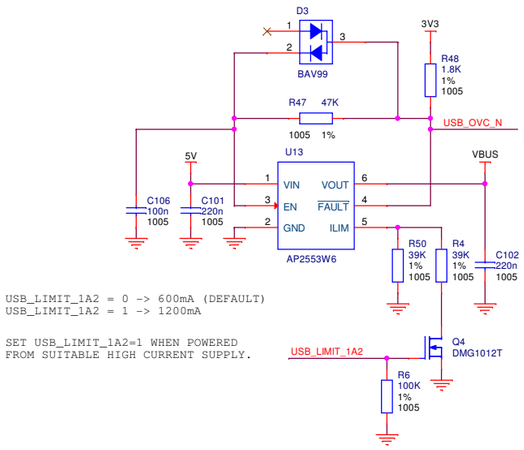

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